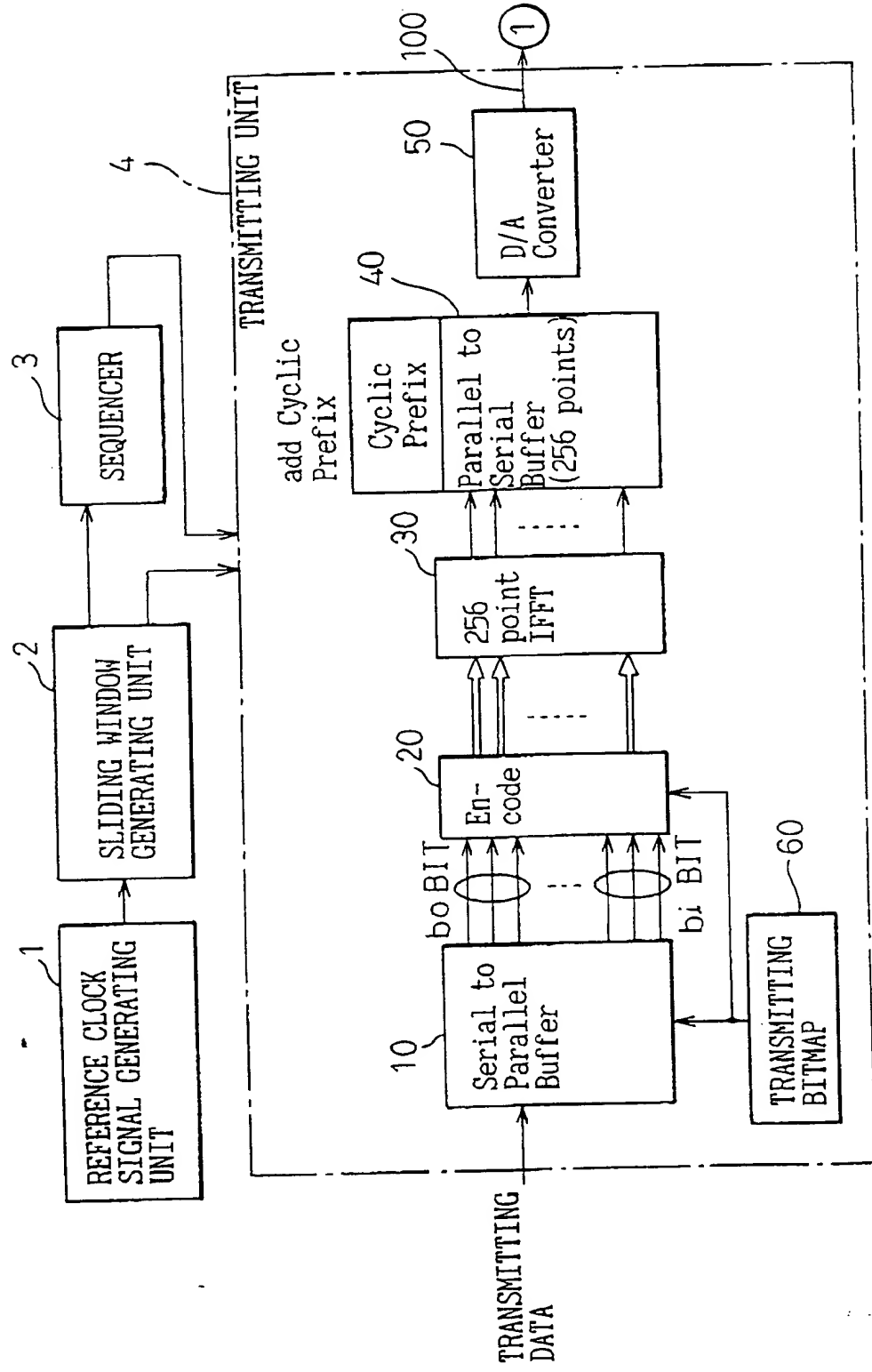


Fig.1A



5.

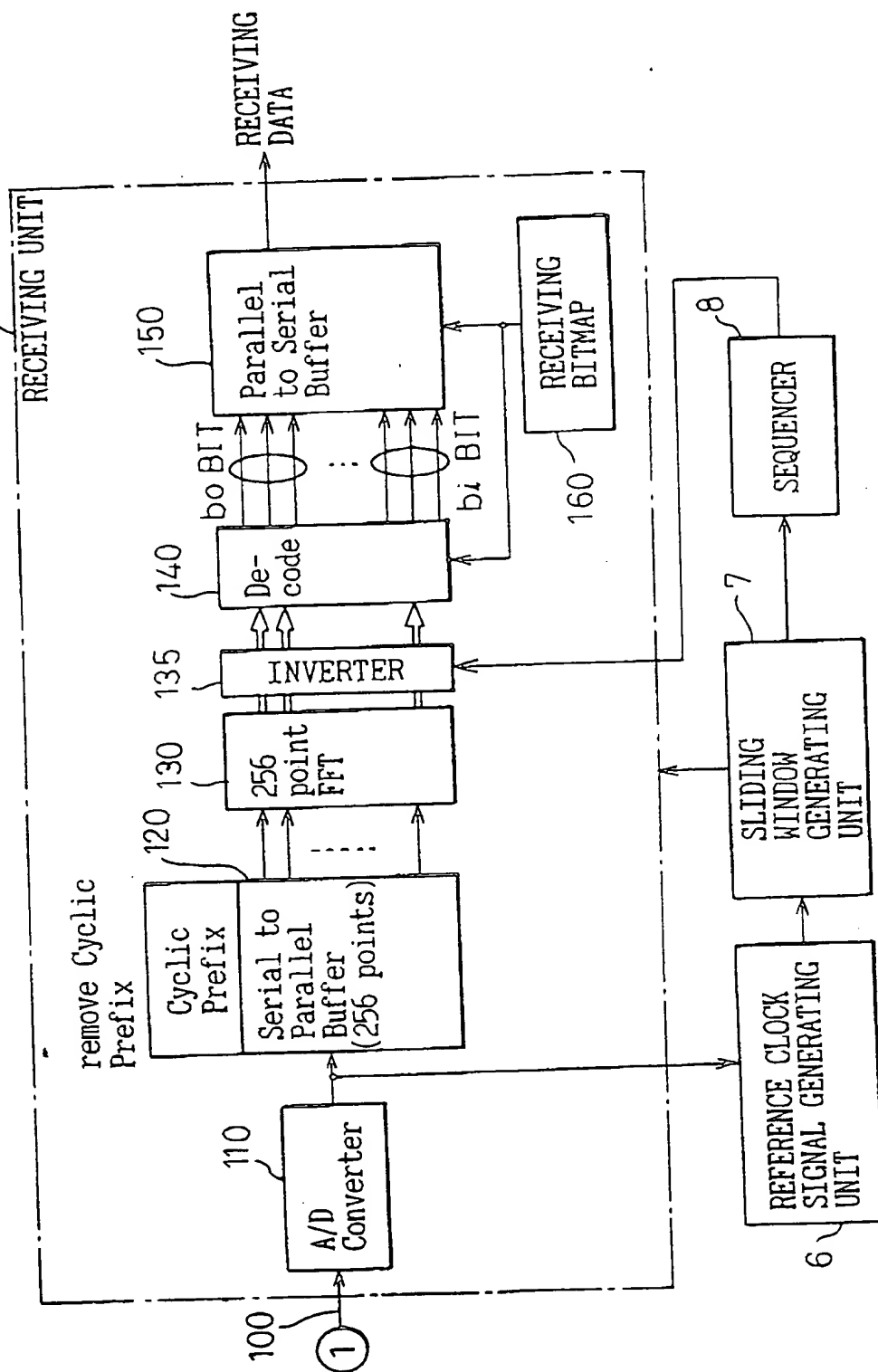


Fig. 2

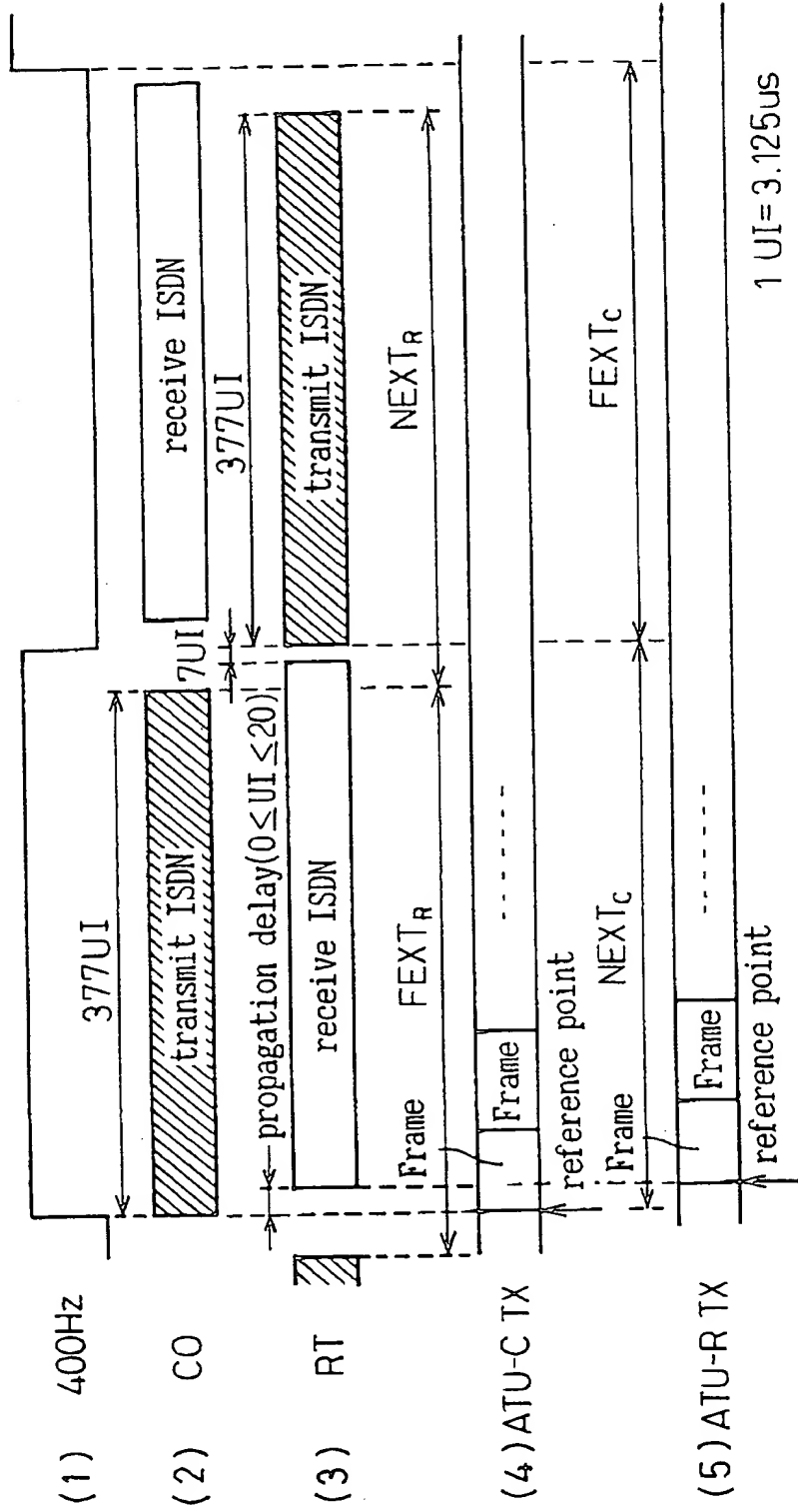
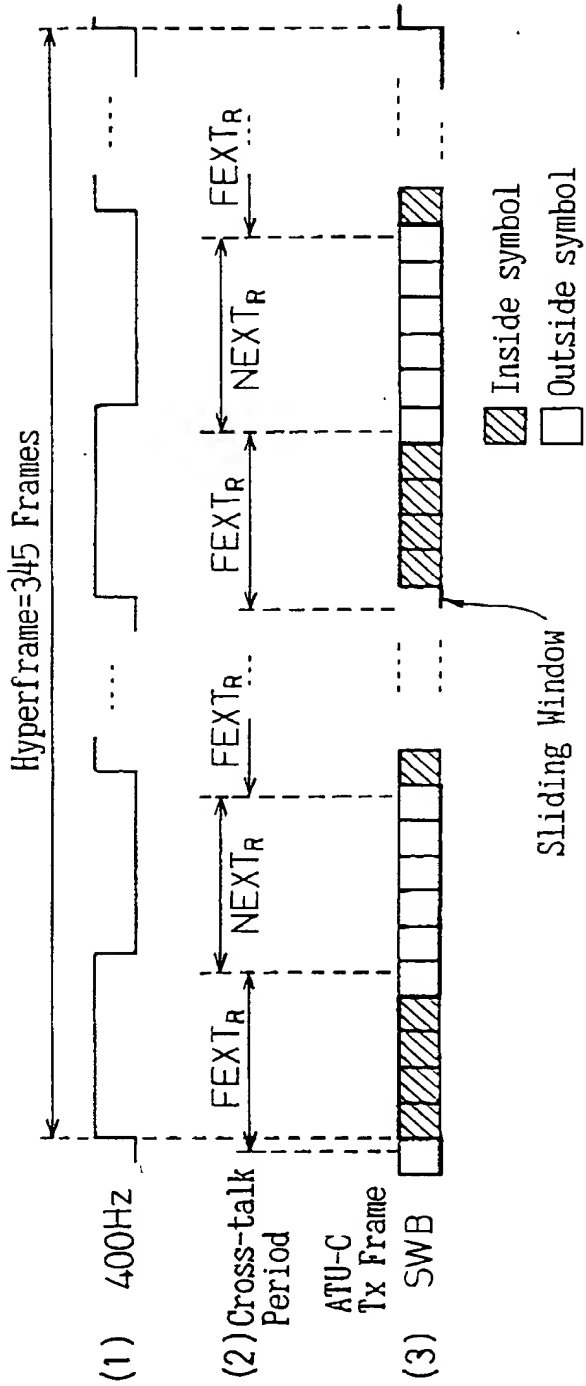
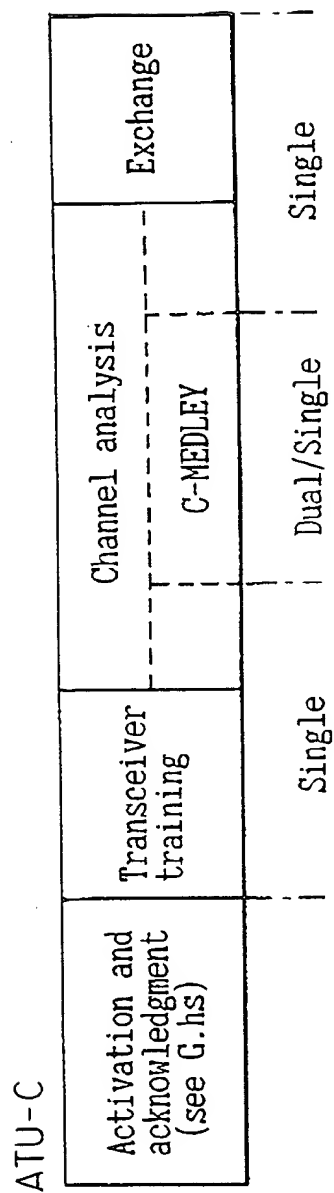


Fig. 3



47. 51.



74B

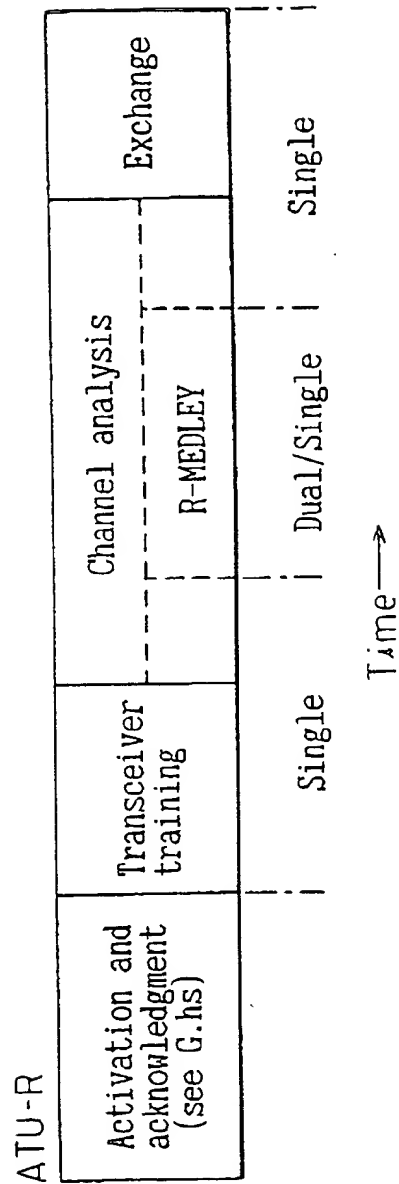


Fig. 5

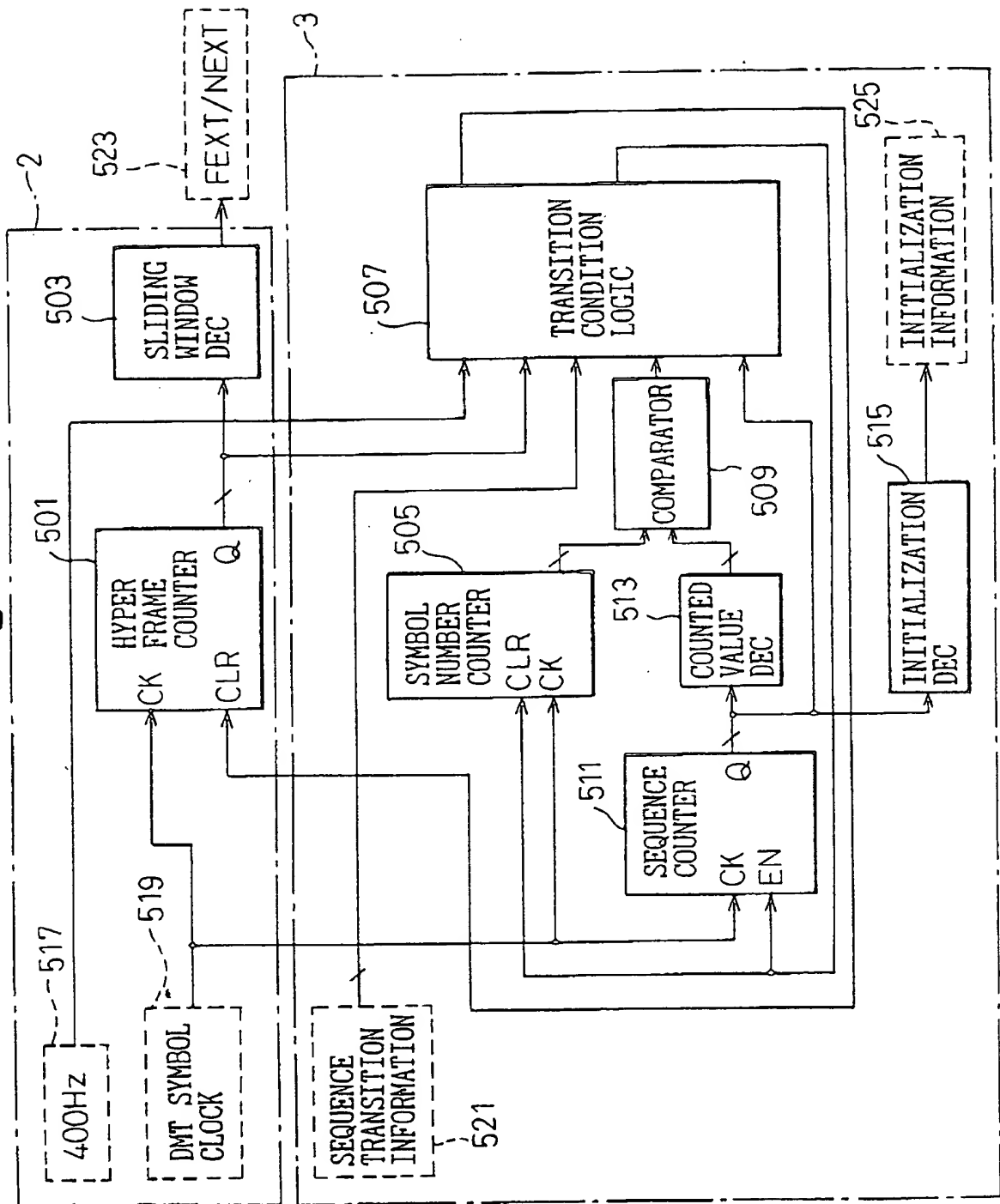


Fig.6

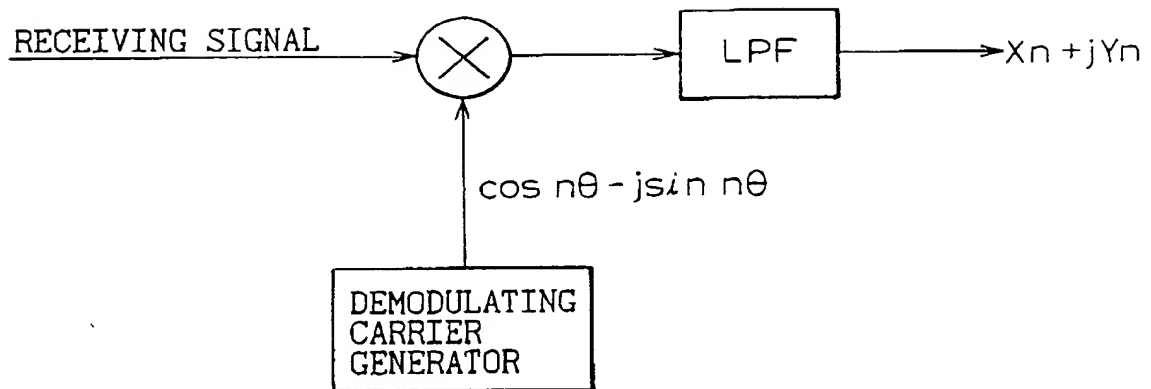


Fig. 7

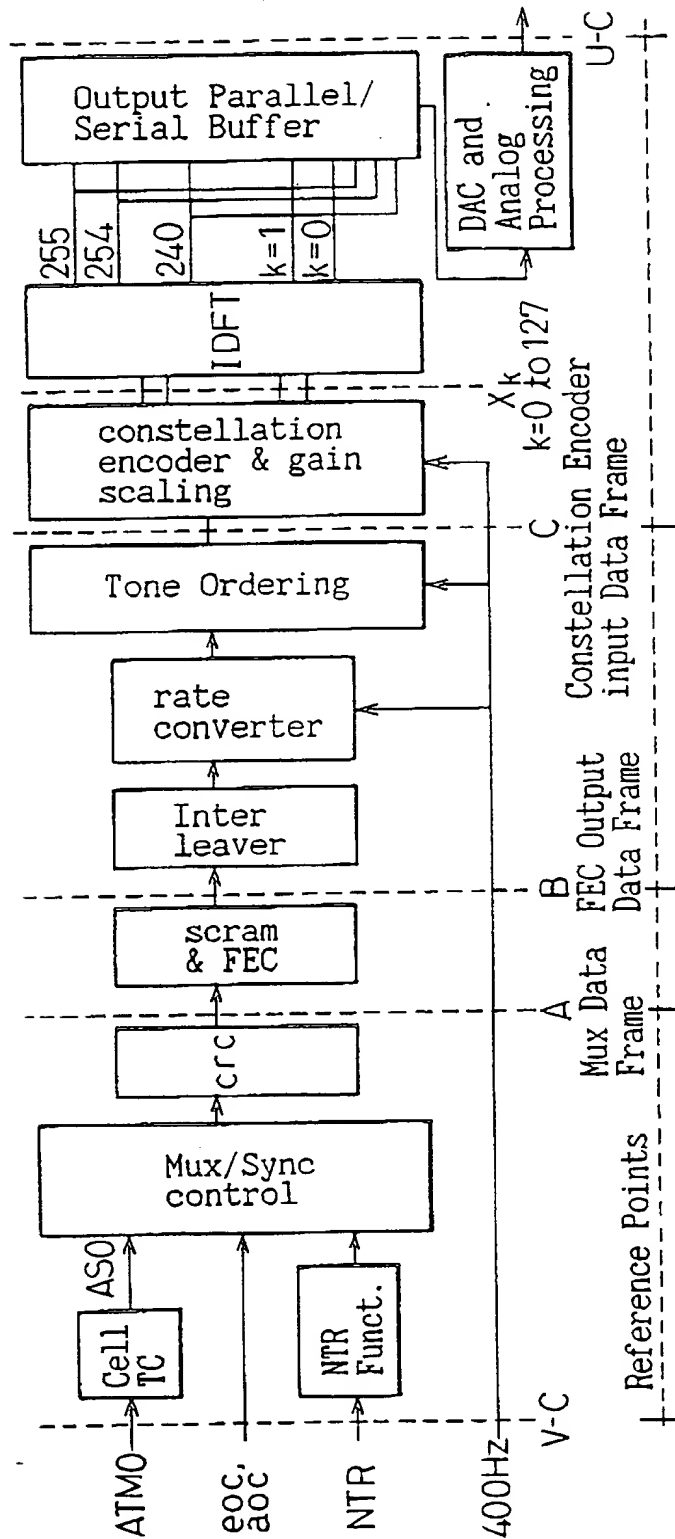


Fig. 8

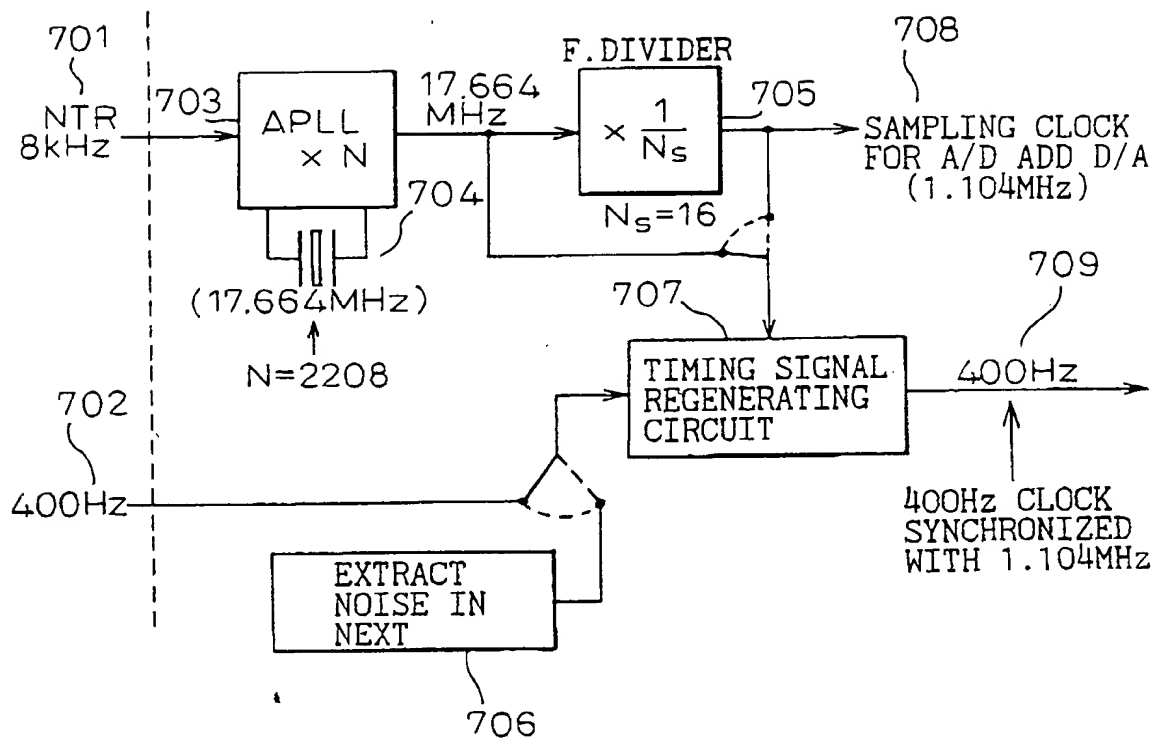
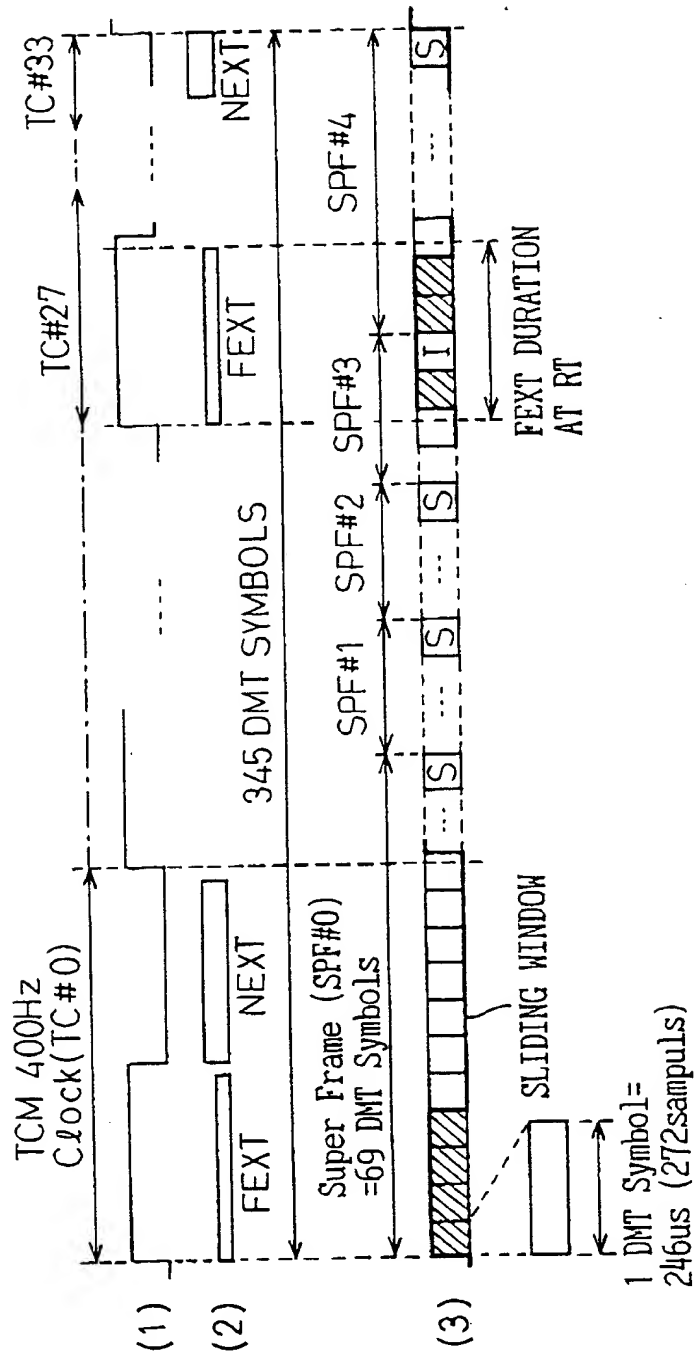


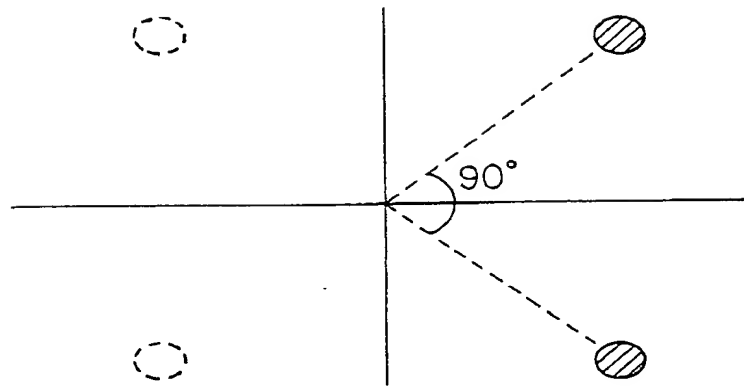
Fig.9



The diagram illustrates the timing of a TCM 400Hz Clock (TC#0) relative to two time intervals, TC#6 and TC#33. The clock signal is shown as a square wave. Below the clock, three data frames are depicted: (1) NEXT, (2) FEXT, and (3) SUPER(SPF #0). The NEXT frame consists of two blocks labeled NEXT and FEXT. The FEXT frame consists of a single block labeled FEXT. The SUPER(SPF #0) frame is divided into four subframes labeled SPF#1, SPF#2, SPF#3, and SPF#4. Each subframe contains a block labeled S. The diagram also shows a shaded area labeled FEXT DURATION AT CO. The timing is defined by dashed lines corresponding to the clock edges and the boundaries of the data frames.

$$\frac{12}{15}$$

Fig.11



14/15

Fig.13A
PRIOR ART

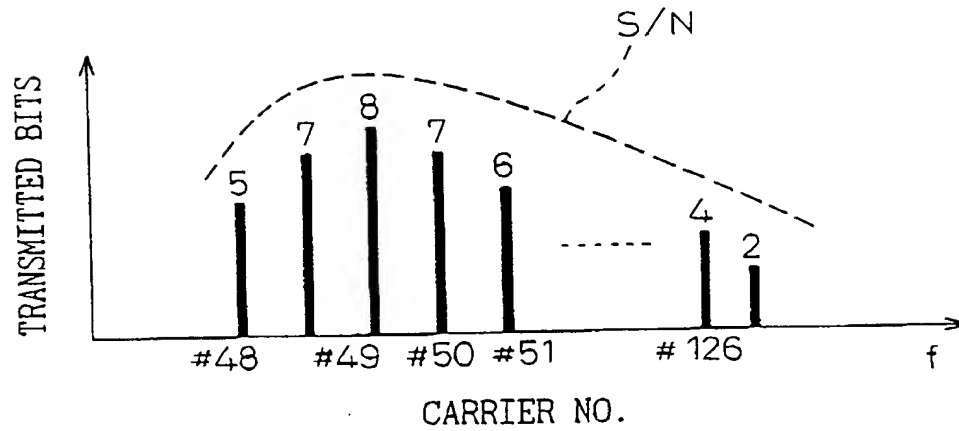


Fig.13B
PRIOR ART

CARRIER NO.i	NUMBER OF TRANSMITTED BITS bi
0	0
⋮	⋮
48	5
49	7
⋮	⋮

Fig.14
PRIOR ART

